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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/760,440	01/20/2004	George E. Hanson	03-1597 9224	
24319	7590 06/01/2005		EXAMINER	
LSI LOGIC CORPORATION 1621 BARBER LANE			BERHANE, ADOLF D	
MS: D-106			ART UNIT	PAPER NUMBER
MILPITAS,	MILPITAS, CA 95035			
			DATE MAILED: 06/01/2005	

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)				
	10/760,440	HANSON, GEORGE E.				
Office Action Summary	Examiner	Art Unit				
	Adolf Berhane	2838				
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply						
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.  - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.  - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.  - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.  - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).  Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).						
Status						
1) Responsive to communication(s) filed on	Responsive to communication(s) filed on					
·—	This action is <b>FINAL</b> . 2b)⊠ This action is non-final.					
,	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.					
Disposition of Claims						
4) ☐ Claim(s) 1-19 is/are pending in the application. 4a) Of the above claim(s) is/are withdrawn from consideration.  5) ☐ Claim(s) is/are allowed.  6) ☐ Claim(s) 1-19 is/are rejected.  7) ☐ Claim(s) is/are objected to.  8) ☐ Claim(s) are subject to restriction and/or election requirement.						
Application Papers						
9) The specification is objected to by the Examiner.						
10)☐ The drawing(s) filed on is/are: a)☐ accepted or b)☐ objected to by the Examiner.						
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).						
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).  11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.						
Priority under 35 U.S.C. § 119						
<ul> <li>12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).</li> <li>a) All b) Some * c) None of:</li> <li>1. Certified copies of the priority documents have been received.</li> <li>2. Certified copies of the priority documents have been received in Application No</li> <li>3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).</li> <li>* See the attached detailed Office action for a list of the certified copies not received.</li> </ul>						
Attachment(s)						
1) Notice of References Cited (PTO-892)  4) Interview Summary (PTO-413)						
<ul> <li>2) Notice of Draftsperson's Patent Drawing Review (PTO-948)</li> <li>3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)</li> <li>Paper No(s)/Mail Date 1/20/04.</li> </ul>	Paper No(s)/Mail Da 5) Notice of Informal P 6) Other:	ate Patent Application (PTO-152)				

## **DETAILED ACTION**

#### Information Disclosure Statement

1. The Examiner has considered the information disclosure statement (IDS) submitted on 1/20/2004.

### Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 3. Claims 1-19 are rejected under 35 U.S.C. 102(b) as being anticipated by Matsushita (6,504,270).

Matsushita disclose an uninterruptible switching regulator in Figs. 1-7. A DC output stage that outputs a predetermined DC electrical power (+12), an AC input stage (1) connected to the DC output stage, with the AC input stage (1) configured to convert AC electrical power at the AC input stage into the predetermined DC electrical power (4) available at the DC output stage, a DC input stage (26) connected to the DC output stage, with the DC input stage configured to convert DC electrical power at the DC input stage into the predetermined DC electrical power (50) available at the DC output stage. the DC output stage comprises at least one capacitor (49) across a DC positive output terminal and a DC ground output terminal, an AC sense circuit (8) that detects AC electrical power at the AC input stage, the DC input stage (30) comprises a DC input stage disable line connected to the AC sense circuit, and wherein the DC input stage

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disable line (34) disables outputting the predetermined DC electrical power from the DC input stage when the AC sense circuit detects AC electrical power at the AC input stage, a switching circuit (69) that receives the DC electrical power and that is configured to generate a substantially AC waveform at a first AC voltage level from the DC electrical power, a transformer (22) connected to the switching circuit and configured to convert the substantially AC waveform at the first AC voltage level to a second AC voltage level; and a full-wave rectifier (4) connected to the transformer and configured to convert the substantially AC waveform at the second AC voltage level to the predetermined DC electrical power, a buffer stage (15 & 41) connected to the switching circuit and configured to provide electrical current to the substantially AC waveform.

#### Conclusion

4. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Adolf Berhane whose telephone number is 571-272-2077. The examiner can normally be reached on Monday- Friday 8 AM to 6 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Michael Sherry can be reached on 571-272-2084. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Adolf Berhane Primary Examiner Art Unit 2838